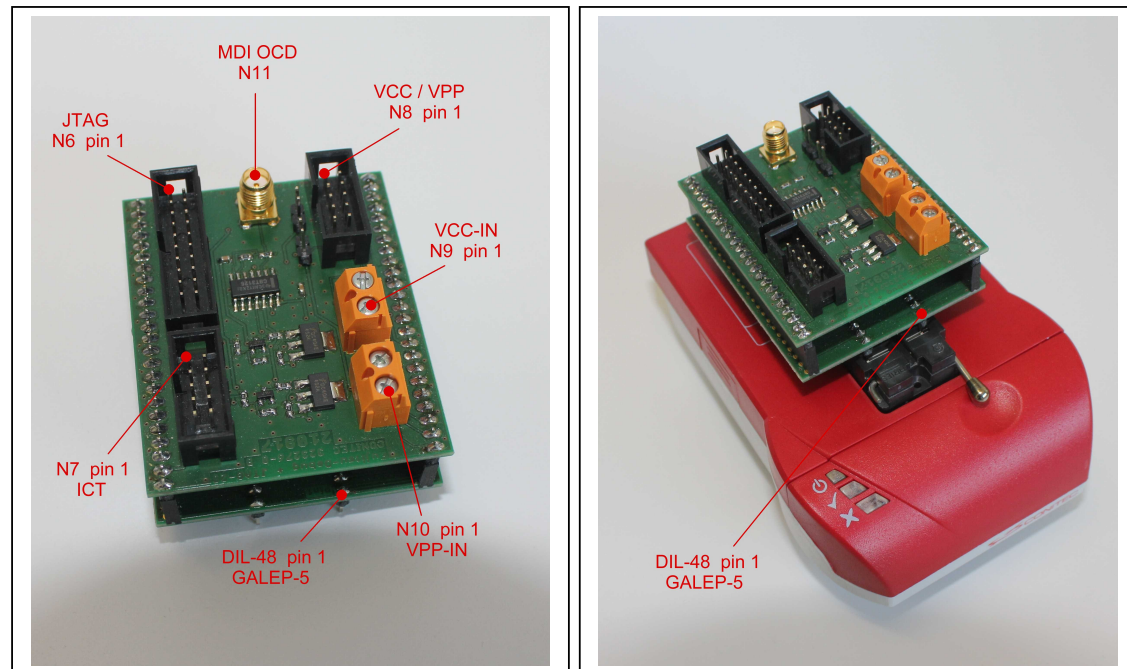


DIL-48 adapter with

- **Debug-Interface (MDI)** for FUJITSU MB91F522, -523, -524, -525 and -526.
- **JTAG-Interface** for various devices programmable by JTAG standard (20 pins).

DIL-48-Adapter mit

- **Debug-Interface (MDI)** für FUJITSU MB91F522, -523, -524, -525 und -526.
- **JTAG-Interface** für verschiedene nach JTAG-Standard (20-polig) zu programmierende Bauteile.



Connector N11 -- MDI / OCD -- coax

-- Center: DEBUGIF / Shield: GND

Connector N6 -- JTAG -- 20 pins

48 pins GALEP-5	20 pins N6	Signal
48	20,18,16,14,12, 10,8,6,4	GND
24	2	Vcc
25	1	TVRef
26	3	nTRST
27	5	TDI
28	7	TMS
29	9	TCK
30	11	RTCK
31	13	TDO
32	15	nRESET
33	17	DBGRQ
34	19	DBGACK

Connector N7 -- ICT -- 10 pins

48 pins GALEP-5.	10 pins N7	Signal	Function
42	1	CRTL0	ICT Control, start PGM switch
43	3	CRTL1	ICT Control, ready state (HI)
44	5	CRTL2	ICT Control, error state (HI)
45	7	CRTL3	ICT Control, PGM end state (HI)
46	9	CRTL4	ICT Control, break switch
48	2,4,6,8,10	GND	Adapter Ground

Connector N8 -- VCC/VPP -- 10 pins

48 pins	GALEP-5.	10 pins	N8	Signal	Function
3		1		SERST_DATA	not implemented
2		3		SERST_CLK	not implemented
-		5		VCC	ext. VCC out
-		7		VPP	not implemented
41		9		READY_CONTOL	not implemented
9		6,8,10		+5V	Adapter supply
48		2,4,6		GND	Adapter Ground

Connector N9 -- VCC-IN -- 2 pins

2 pins	N9	Signal	Function
1		VCC_IN	ext. VCC input
2		GND	Adapter Ground

Connector N10 -- VPP-IN -- 2 pins

2 pins	N10	Signal	Function
1		VPP_IN	ext. VPP input
2		GND	Adapter Ground